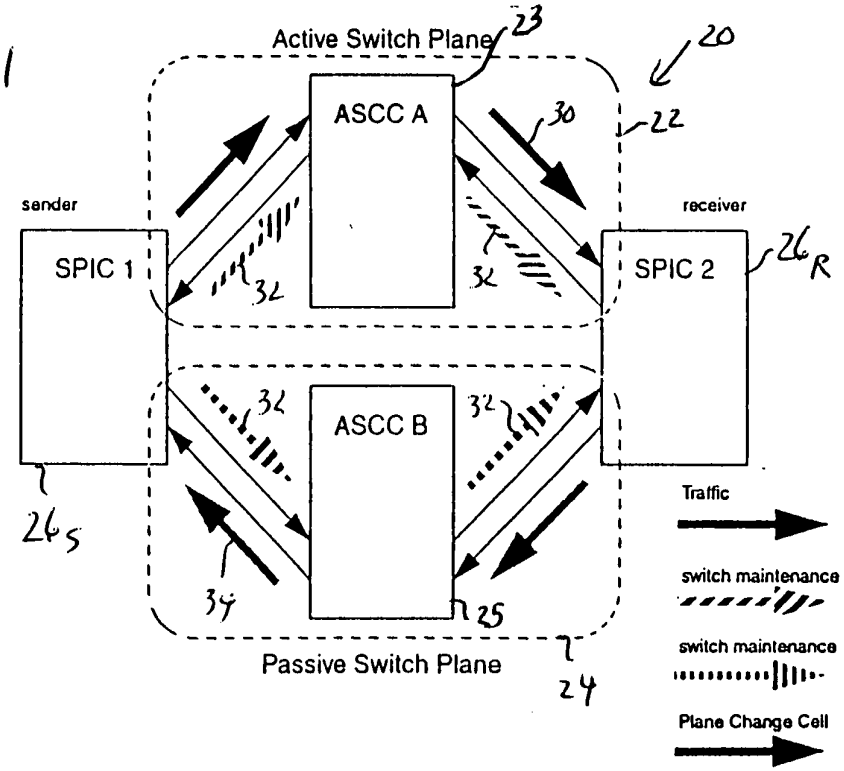
DOCKET: 2STB8960

Fig 1



0091017 25T30960

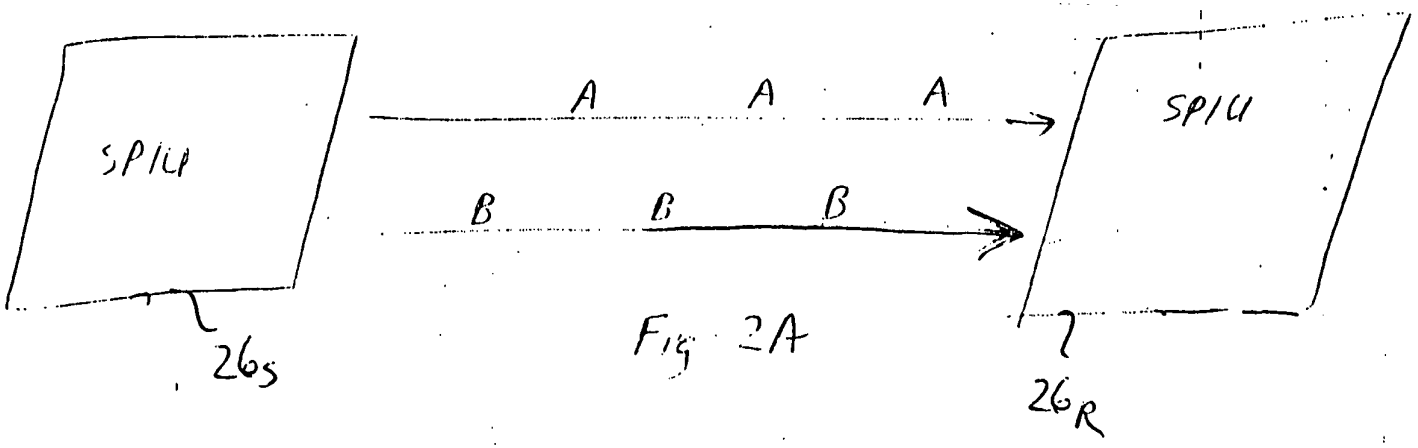


Fig 2A

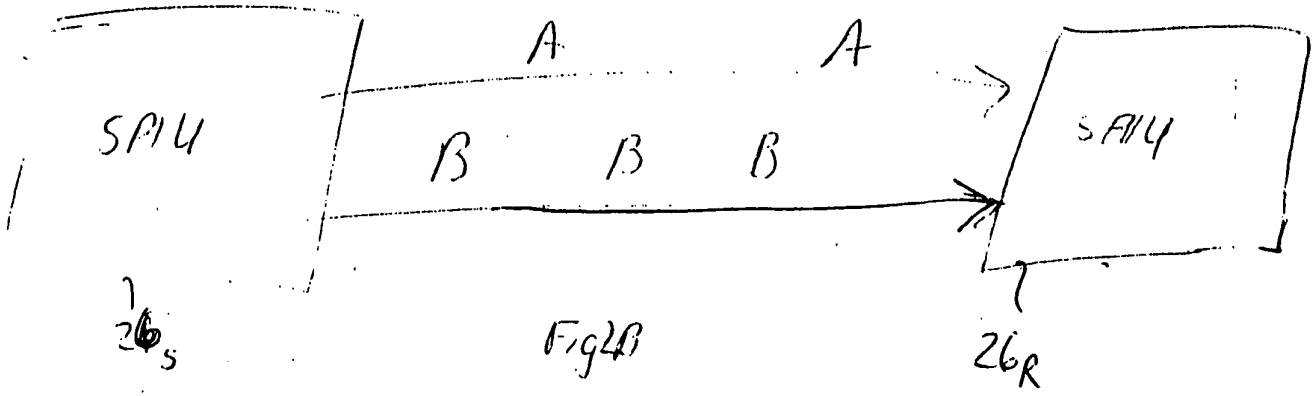


Fig 2B

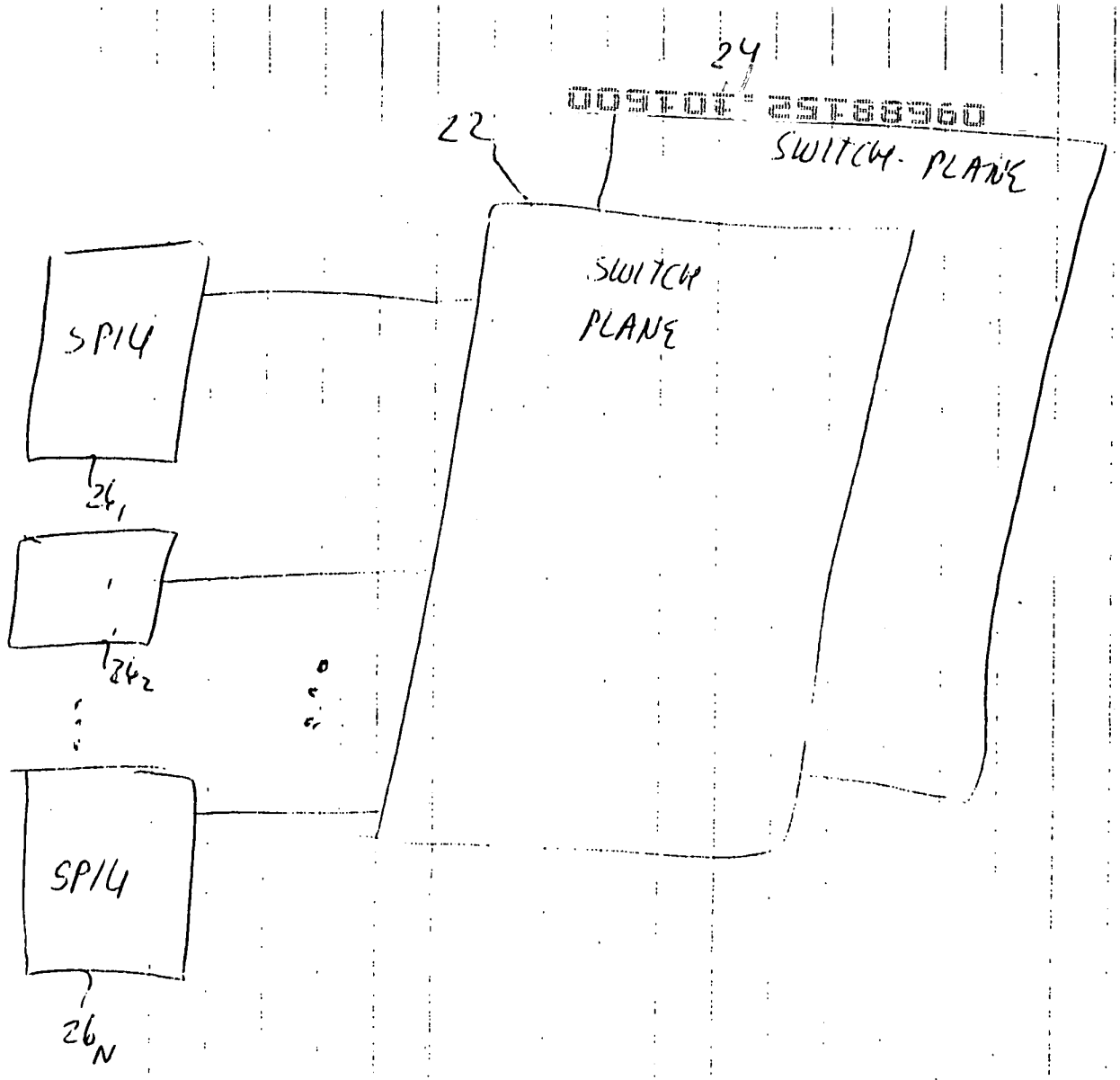

24
320
22
SWITCH PLANE
SWITCH PLANE
SPI4
SPI4
SPI4
26_1
26_2
26_N

Fig 3

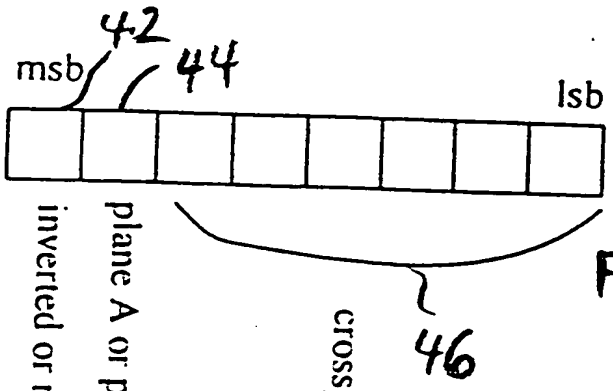

42
msb
44
lsb
40
inverted or not inverted maintenance cell
plane A or plane B
crosspoint address
46

Fig 4A

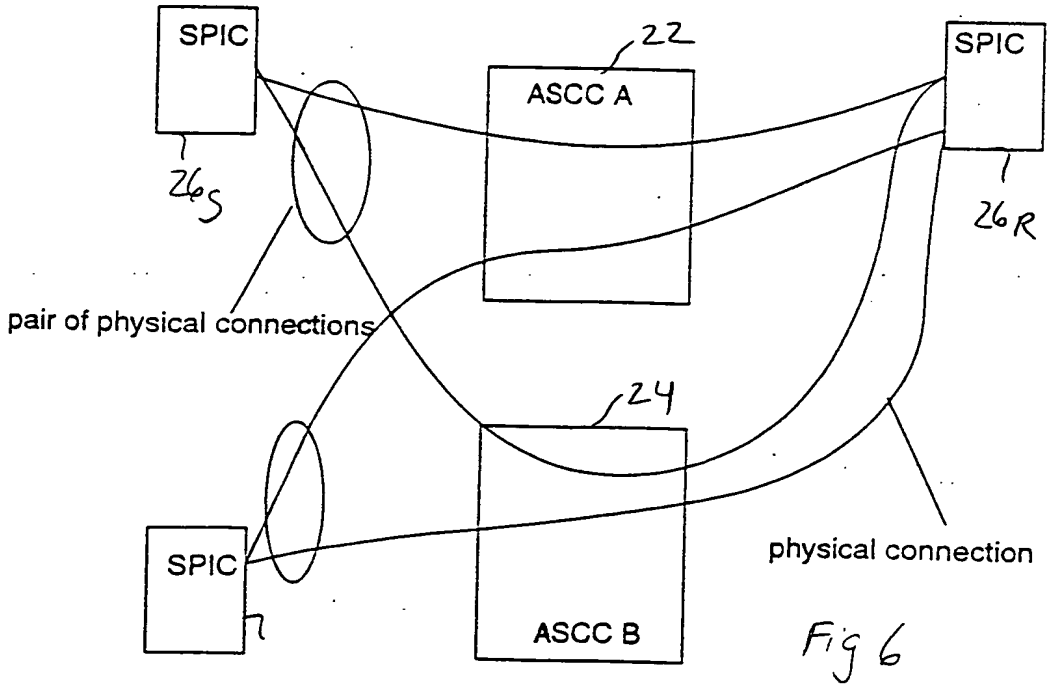

SPIC
22
ASCC A
SPIC
26S
26R
pair of physical connections
24
SPIC
physical connection
ASCC B

Fig 6

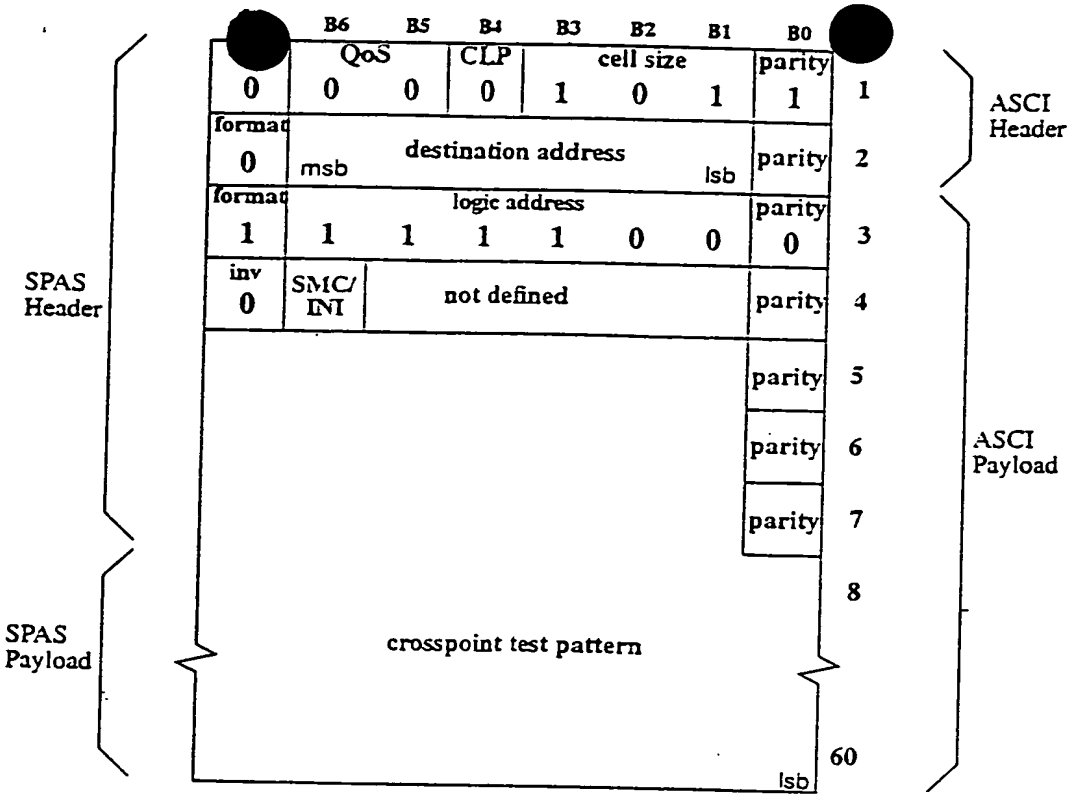


Fig 4B(1) **Initialisation Cell**

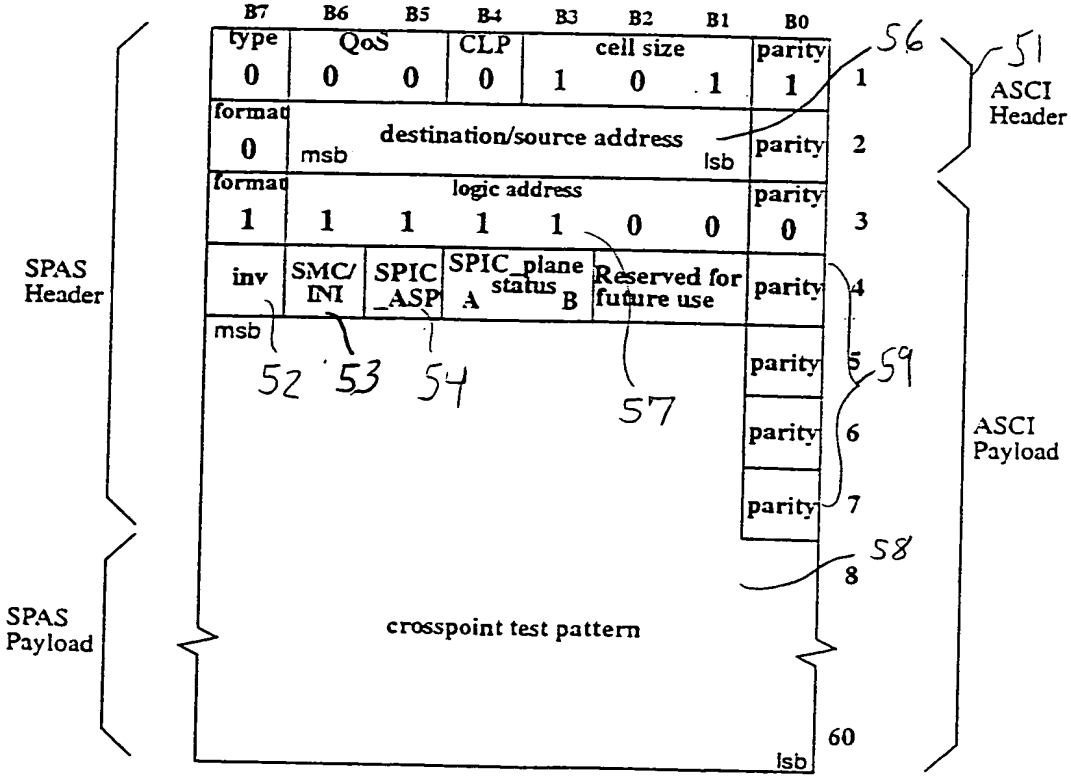


Fig 4B(2) **Switch Maintenance Cell**

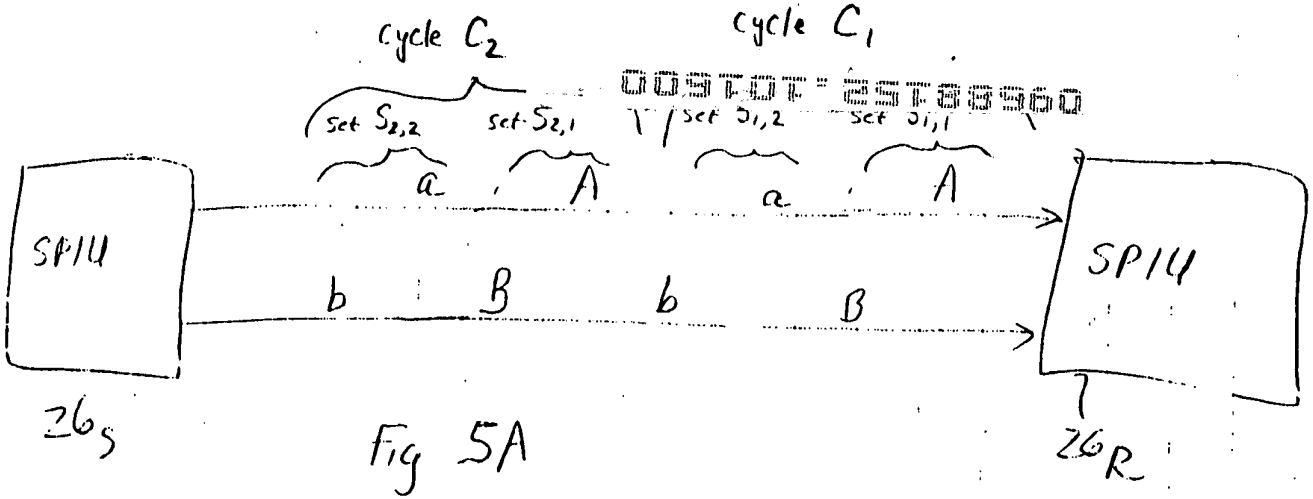
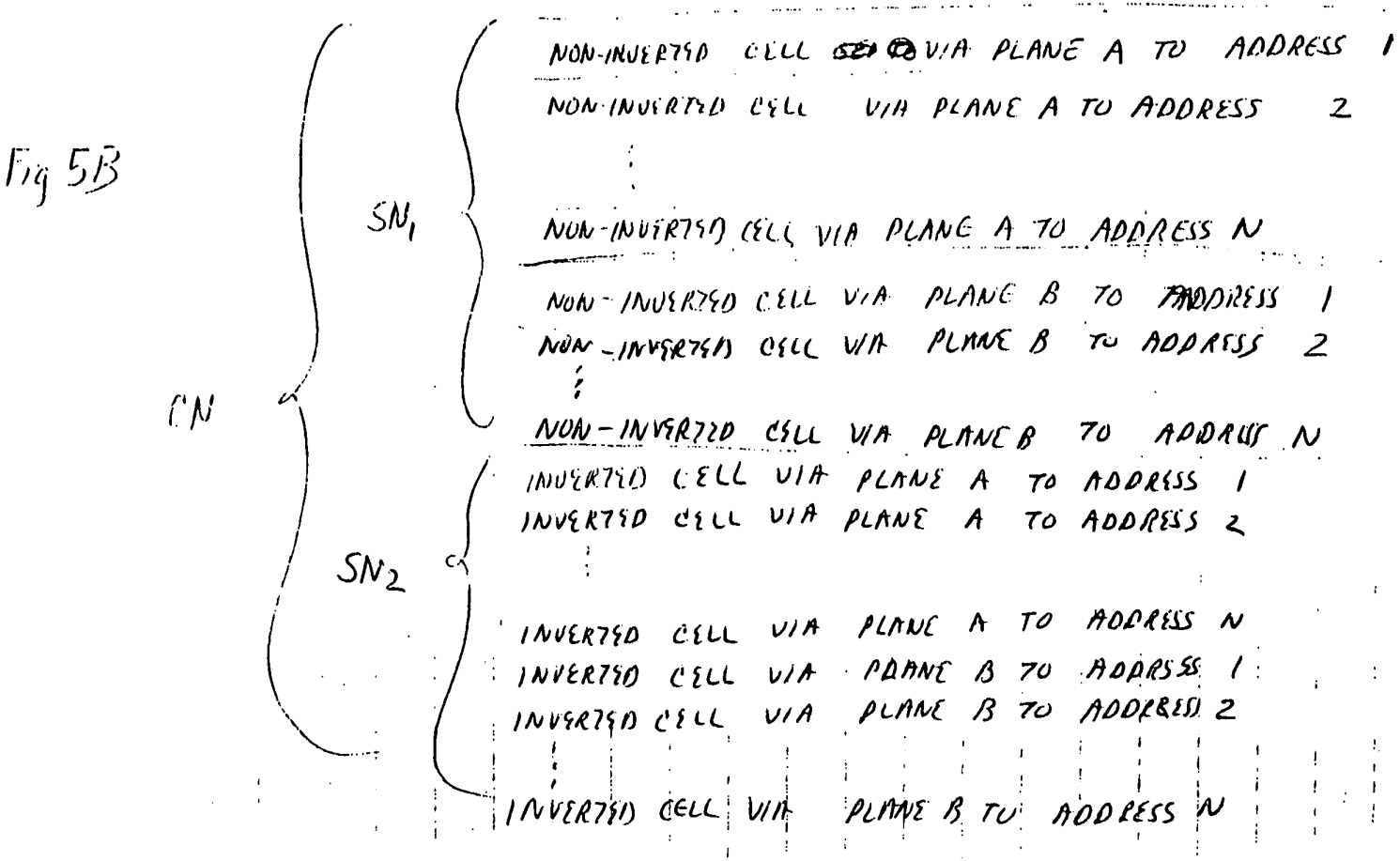

cycle C_2
cycle C_1
set $S_{2,2}$
set $S_{2,1}$
set $S_{1,2}$
set $S_{1,1}$
a
A
a
A
b
B
b
B
SPIU
SPIU
26_S
26_R

Fig 5A

Fig 5B


NON-INVERTED CELL VIA PLANE A TO ADDRESS 1
NON-INVERTED CELL VIA PLANE A TO ADDRESS 2
NON-INVERTED CELL VIA PLANE A TO ADDRESS N
NON-INVERTED CELL VIA PLANE B TO ADDRESS 1
NON-INVERTED CELL VIA PLANE B TO ADDRESS 2
NON-INVERTED CELL VIA PLANE B TO ADDRESS N
INVERTED CELL VIA PLANE A TO ADDRESS 1
INVERTED CELL VIA PLANE A TO ADDRESS 2
INVERTED CELL VIA PLANE A TO ADDRESS N
INVERTED CELL VIA PLANE B TO ADDRESS 1
INVERTED CELL VIA PLANE B TO ADDRESS 2
INVERTED CELL VIA PLANE B TO ADDRESS N
SN_1
SN_2
CN

DOCKET 25788960

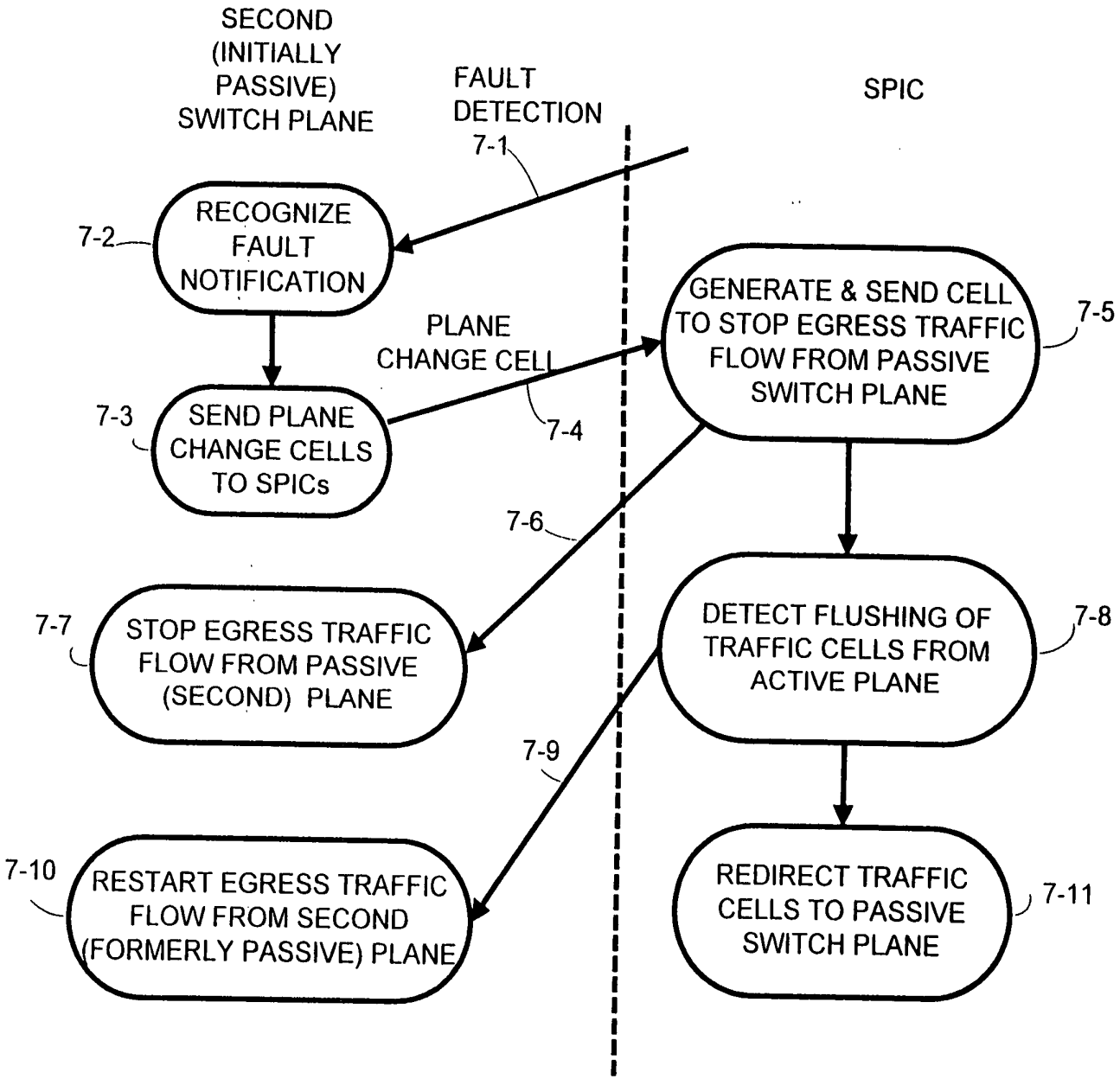


Fig. 7

0097OT-2578960

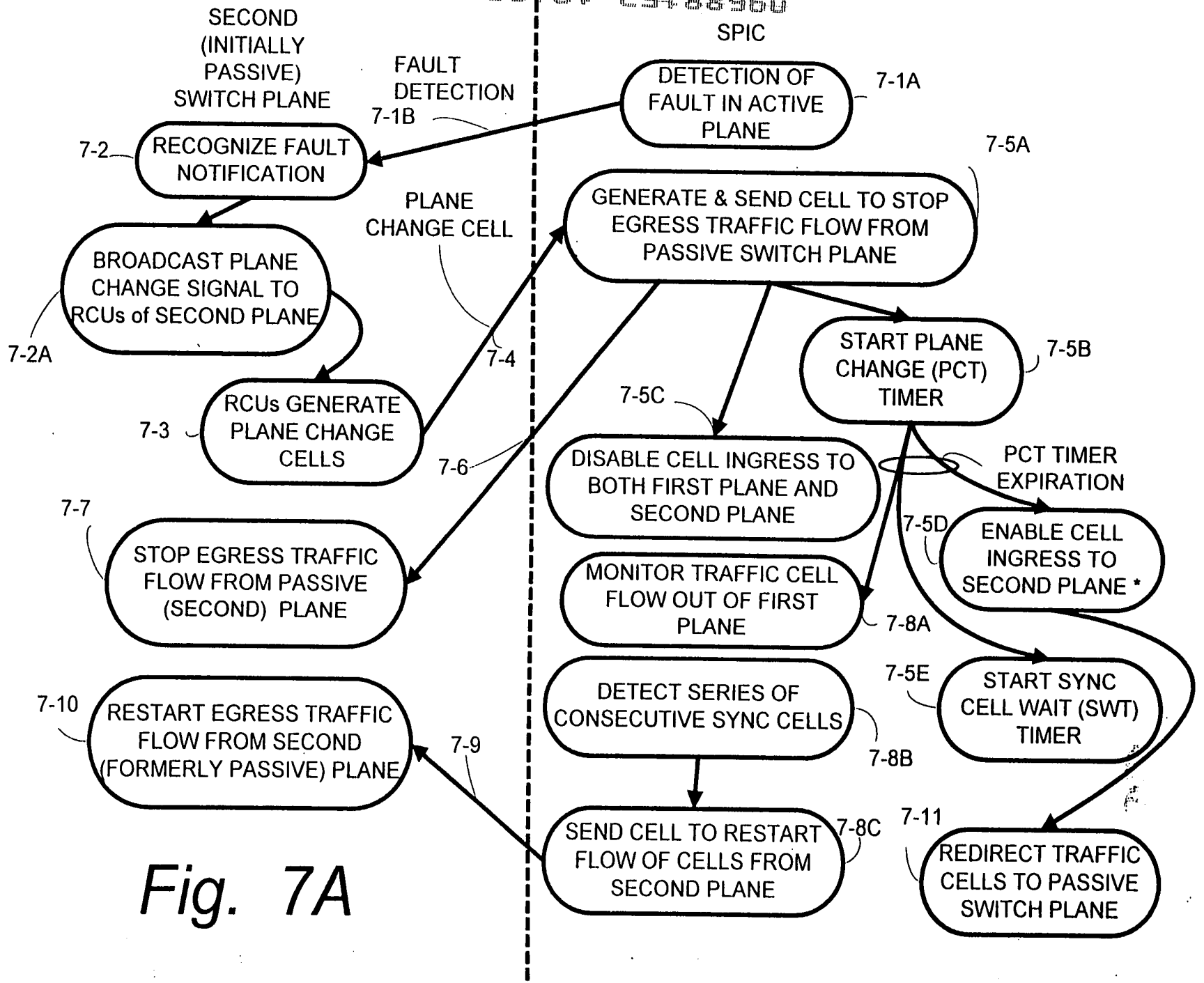


Fig. 7A

0097017-2518960

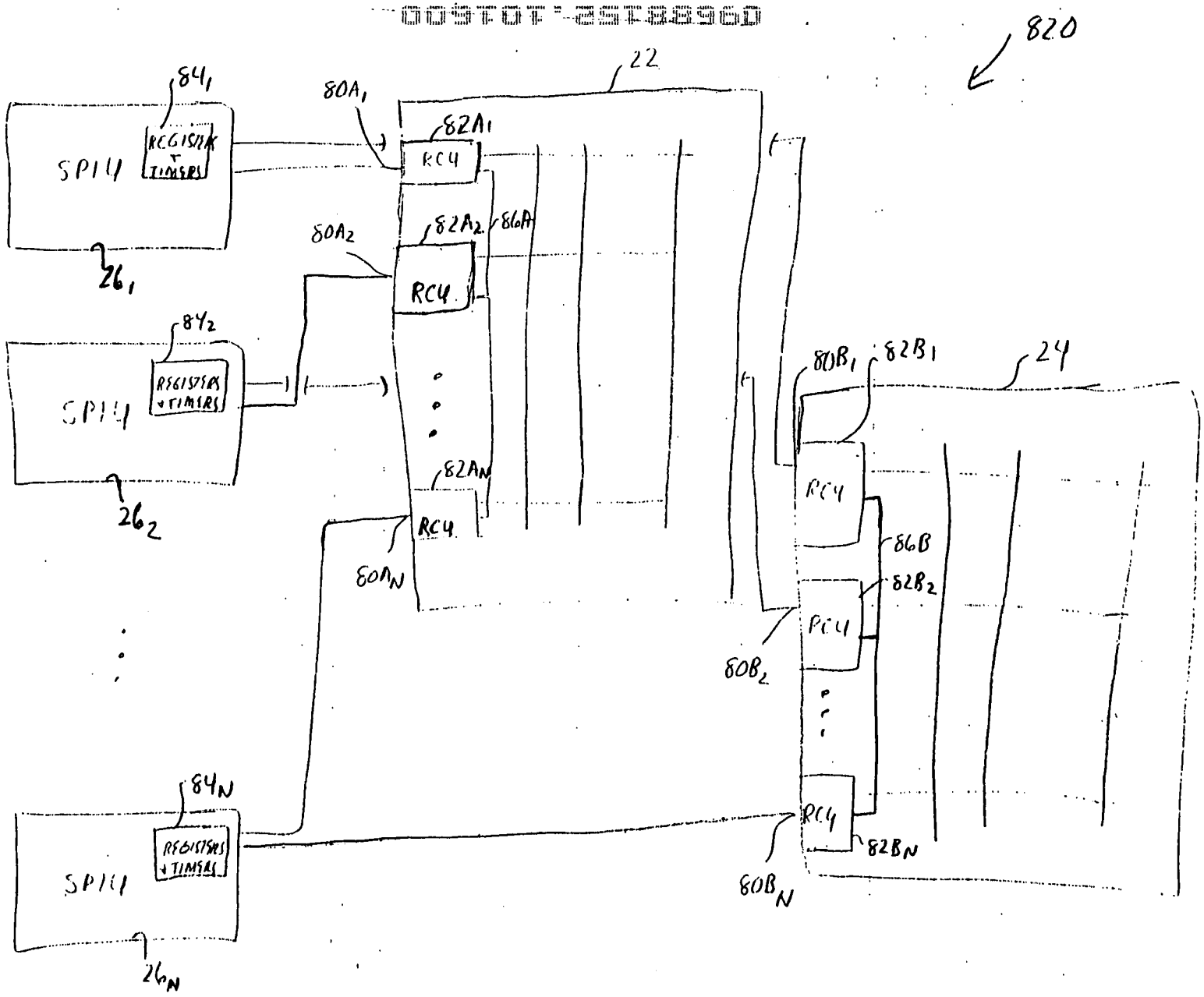


Fig 8

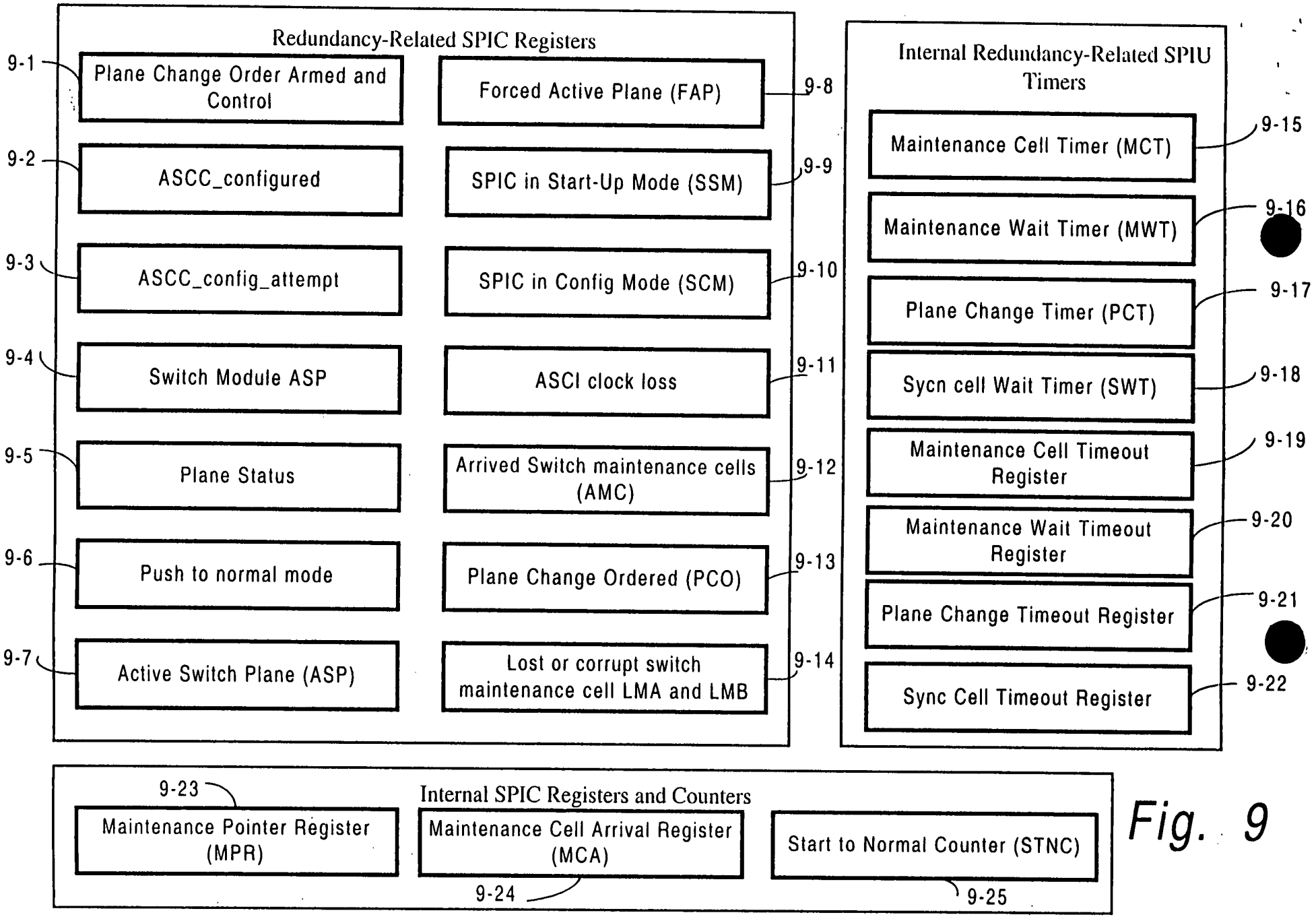

Redundancy-Related SPIC Registers
9-1
Plane Change Order Armed and Control
9-2
ASCC_configured
9-3
ASCC_config_attempt
9-4
Switch Module ASP
9-5
Plane Status
9-6
Push to normal mode
9-7
Active Switch Plane (ASP)
Forced Active Plane (FAP)
9-8
SPIC in Start-Up Mode (SSM)
9-9
SPIC in Config Mode (SCM)
9-10
ASCI clock loss
9-11
Arrived Switch maintenance cells (AMC)
9-12
Plane Change Ordered (PCO)
9-13
Lost or corrupt switch maintenance cell LMA and LMB
9-14
Internal Redundancy-Related SPIU Timers
Maintenance Cell Timer (MCT)
9-15
Maintenance Wait Timer (MWT)
9-16
Plane Change Timer (PCT)
9-17
Sycn cell Wait Timer (SWT)
9-18
Maintenance Cell Timeout Register
9-19
Maintenance Wait Timeout Register
9-20
Plane Change Timeout Register
9-21
Sync Cell Timeout Register
9-22
Internal SPIC Registers and Counters
9-23
Maintenance Pointer Register (MPR)
Maintenance Cell Arrival Register (MCA)
9-24
Start to Normal Counter (STNC)
9-25

Fig. 9

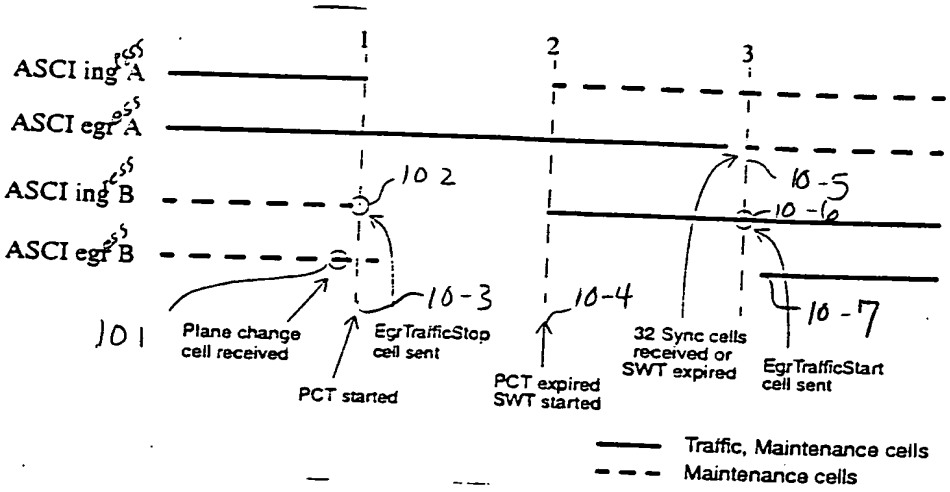
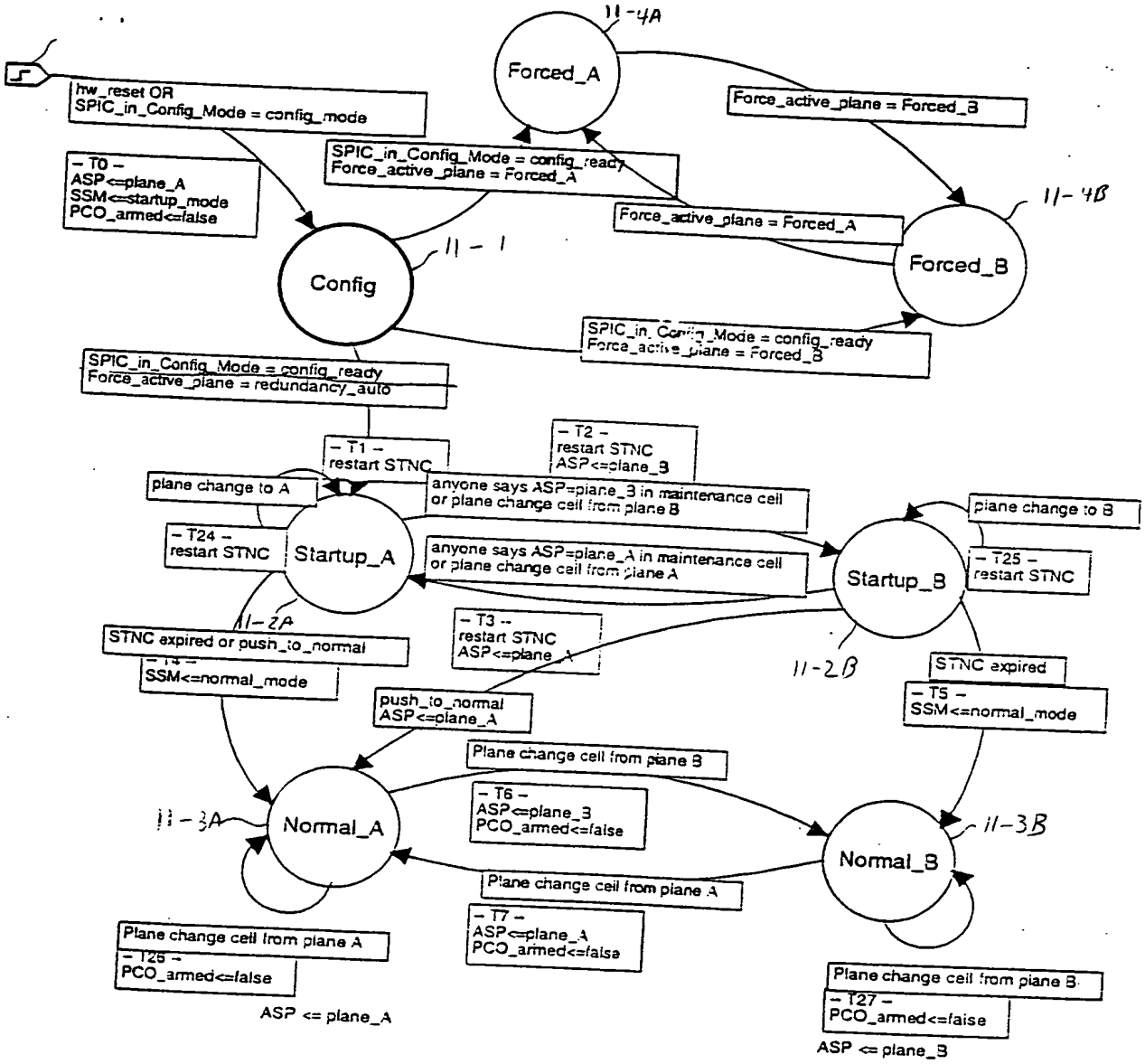

1
2
3
ASCI ingress A
ASCI egress A
ASCI ingress B
ASCI egress B
10 2
10-5
10-6
10-3
10-4
10-7
101
Plane change cell received
EgrTrafficStop cell sent
PCT started
PCT expired SWT started
32 Sync cells received or SWT expired
EgrTrafficStart cell sent
Traffic, Maintenance cells
Maintenance cells

Fig 10



ASP = Active switch plane
SSM = SPIC in start-up mode
PCO_armed = Plain change order armed
STNC = Start to normal counter

Fig 11

09F0T: 25T8B960

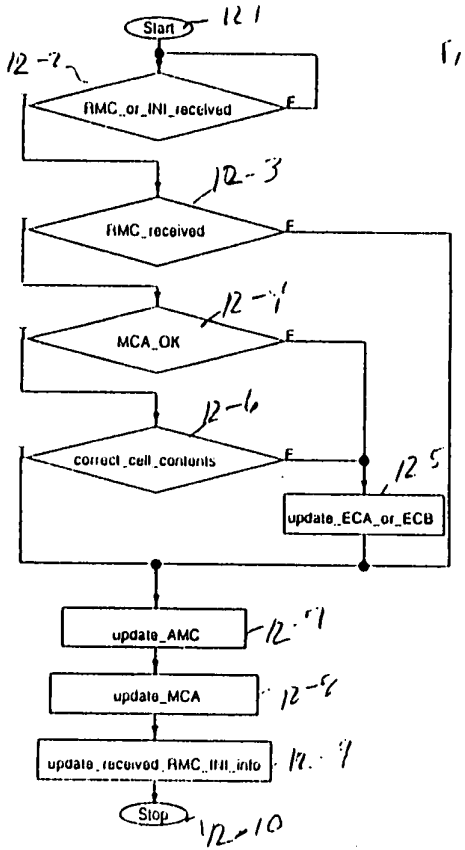


Fig 12

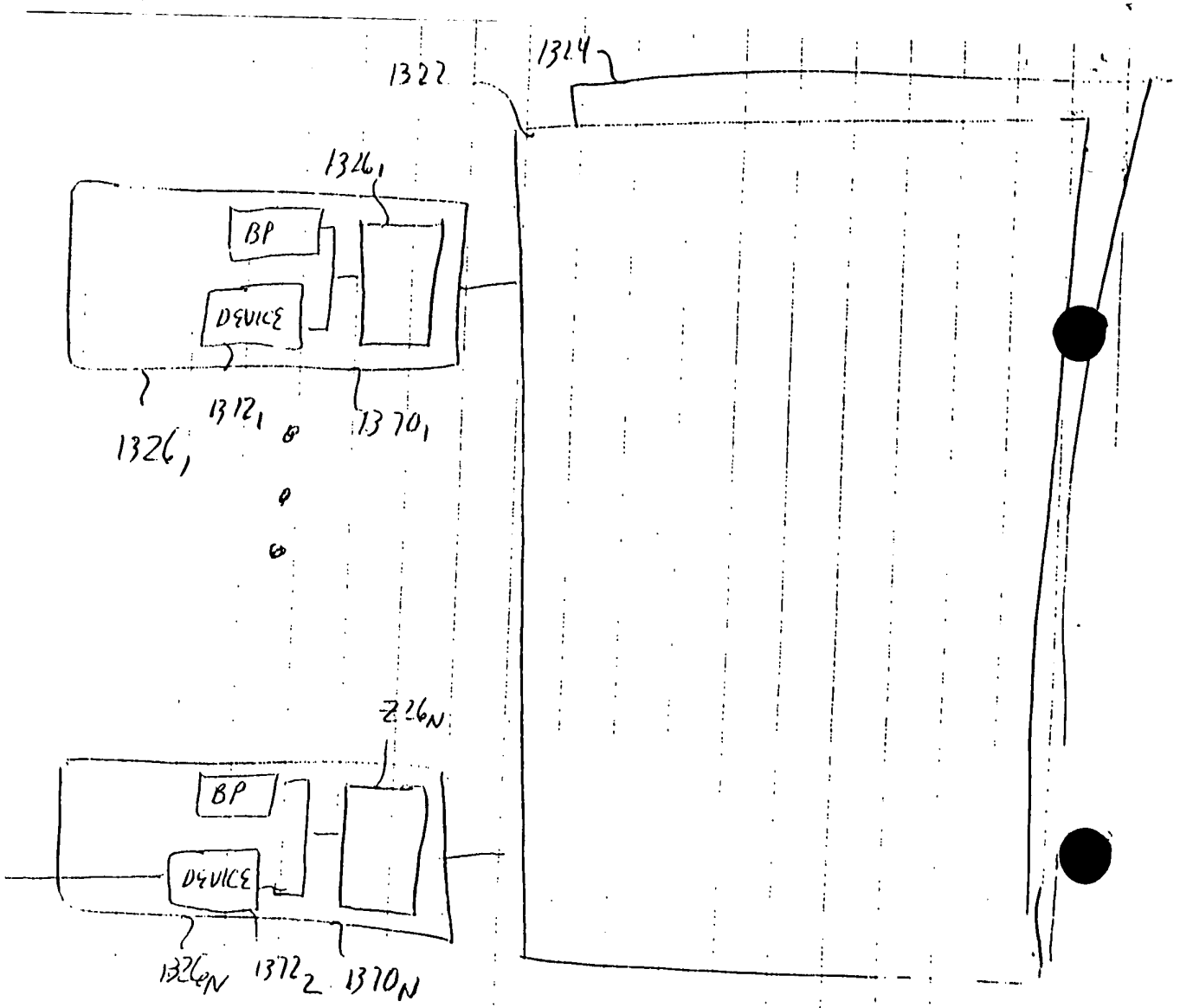


Fig 13